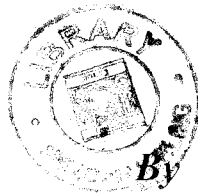


**DEVELOPMENT OF AN EFFICIENT TESTING
METHODOLOGY FOR SYSTEM-ON-CHIP DESIGN**

Thesis submitted for the degree of Doctor of Philosophy
(Engineering)

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